APPLN. FILING DATE: JULY 11, 2001
TITL: NTERFERENCE REJECTION GPS AN SYST
INVENTOR(S): RICHARD KIRCHOFER ET AL.
APPLICATION SERIAL NO: UNASSIGNED SHEET 1 of 14

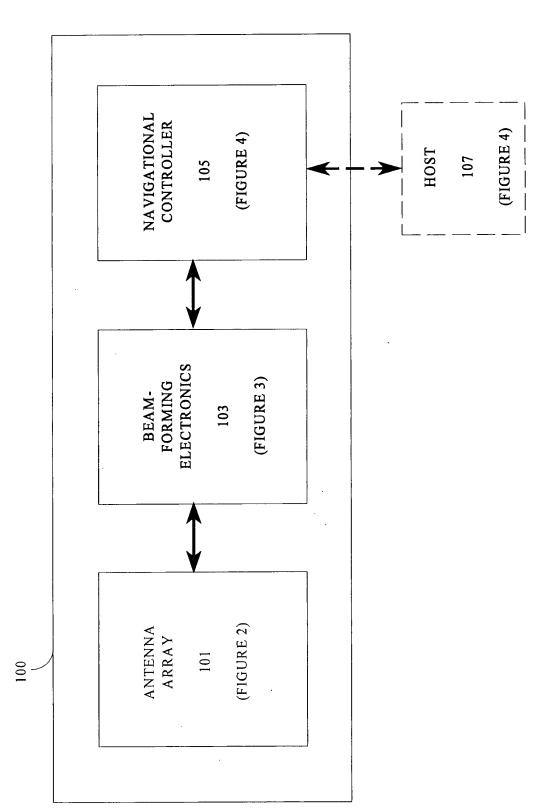


FIGURE 1

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FORMING ELECTRONICS (FIGURE 3) TO BEAM-

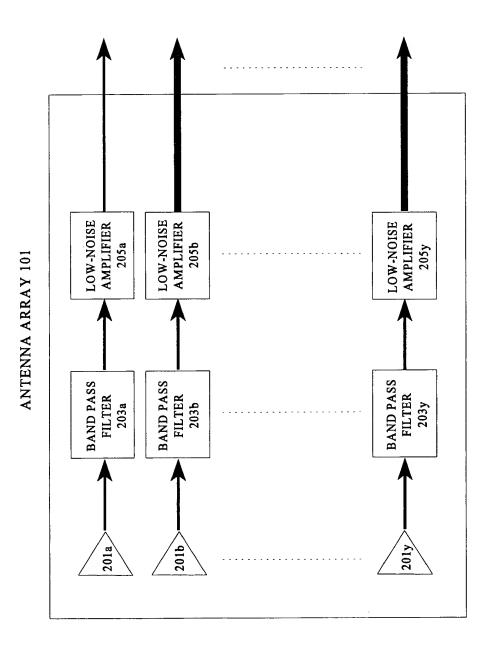


FIGURE 2

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N GPS ENNA

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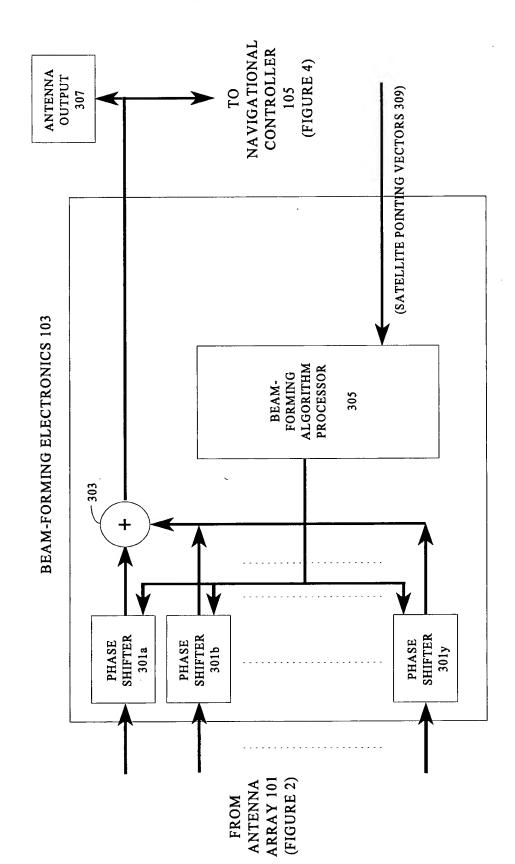


FIGURE 3

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INVENTOR(S): RICHARD KIRCHOFER ET AL.
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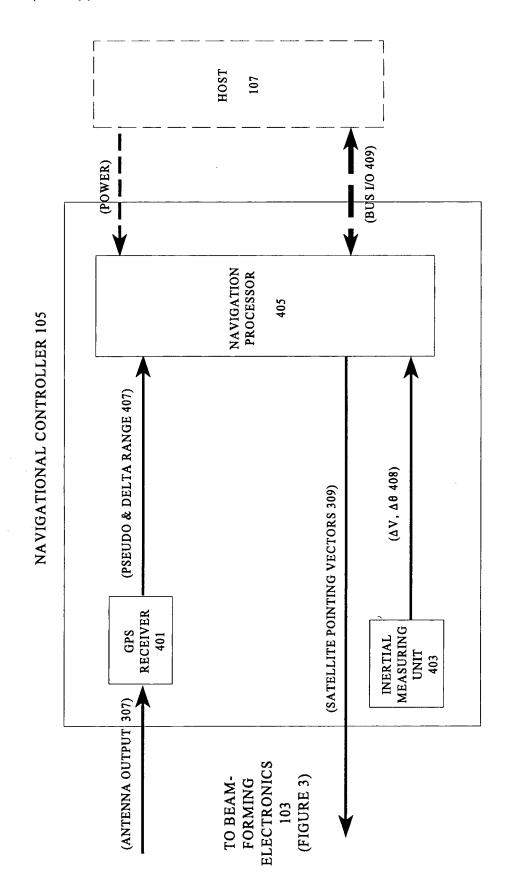


FIGURE 4

APPLN. FILING DATE: JULY 11, 2001
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SYSTEM
INVENTOR(S): RICHARD KIRCHOFER ET AL.
APPLICATION SERIAL NO: UNASSIGNED
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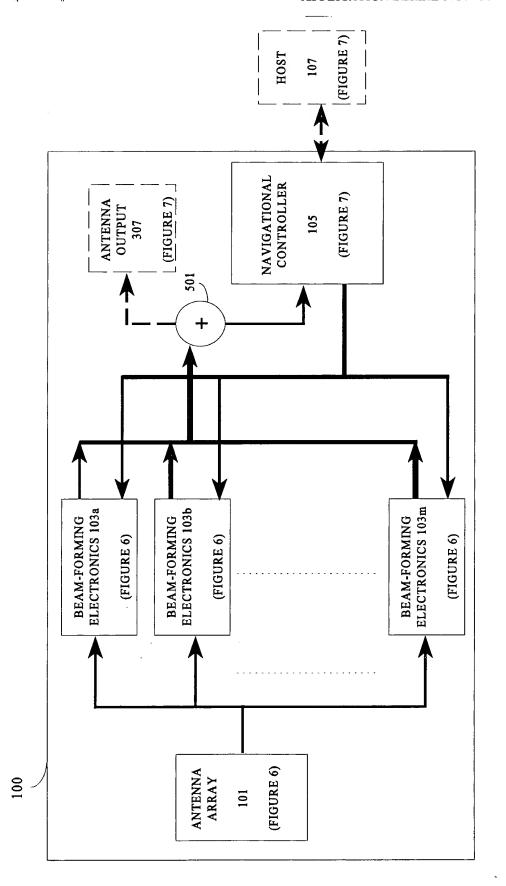


FIGURE 5

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TITLE: INTERFEDENCE REJECTION GPS ANTENNA

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SYSTEM

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HOST 107 (BUS I/O 409) (POWER) NAVIGATION PROCESSOR 405 NAVIGATIONAL CONTROLLER 105 (PSEUDO & DELTA (ΔV, Δθ 408) **RANGE 407)** INERTIAL MEASURING RECEIVER GPS UNIT 401 403 (POINTING VECTOR 309) ANTENNA OUTPUT 307 TO BEAM-FORMING ELECTRONICS 103 (FIGURE 6)

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TITLE: INTERFERENCE REJECTION GPS ANTENNA

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INVENTOR(S): RICHARD KIRCHOFER ET AL. APPLICATION SERIAL NO: UNASSIGNED

SYSTEM

FIGURE 7

DESCRIPTION OF LACT

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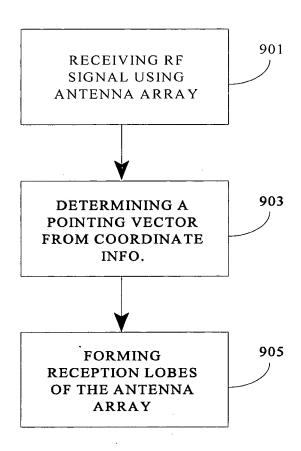


FIGURE 9

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SYS'A...
INVENTOR(S): RICHARD KIRCHOFER ET AL.
APPLICATION SERIAL NO: UNASSIGNED SHEET 10 of 14



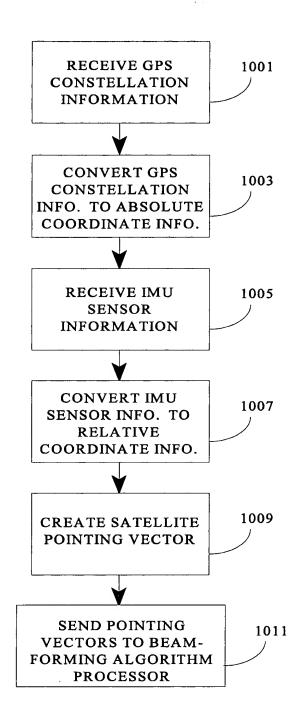


FIGURE 10

INVENTOR(S): RICHARD KIRCHOFER ET AL. APPLICATION SERIAL NO: UNASSIGNED

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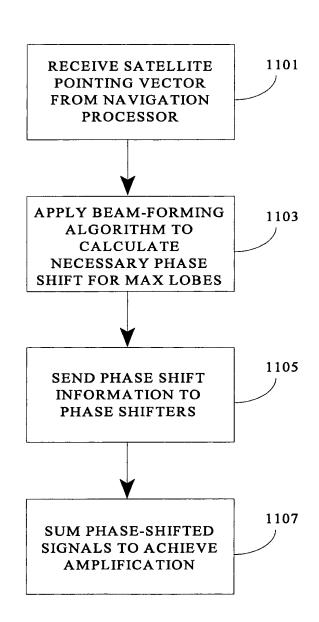


FIGURE 11

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SY M
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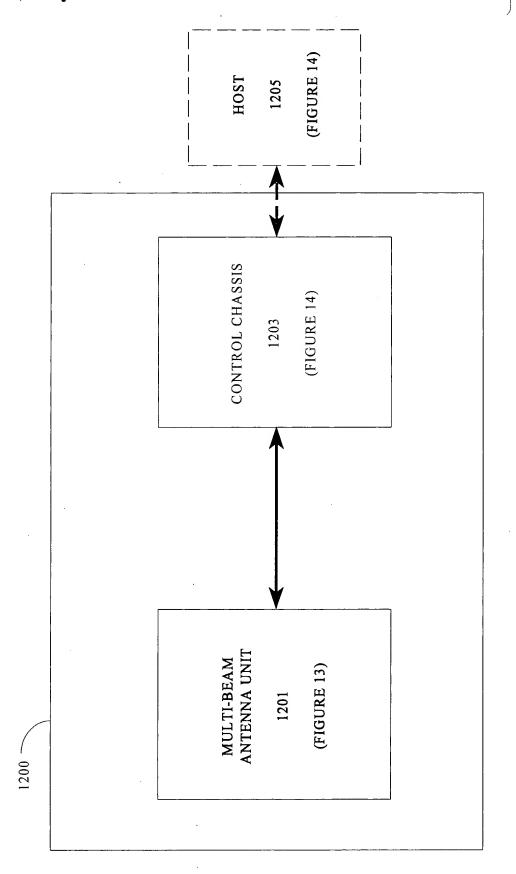


FIGURE 12

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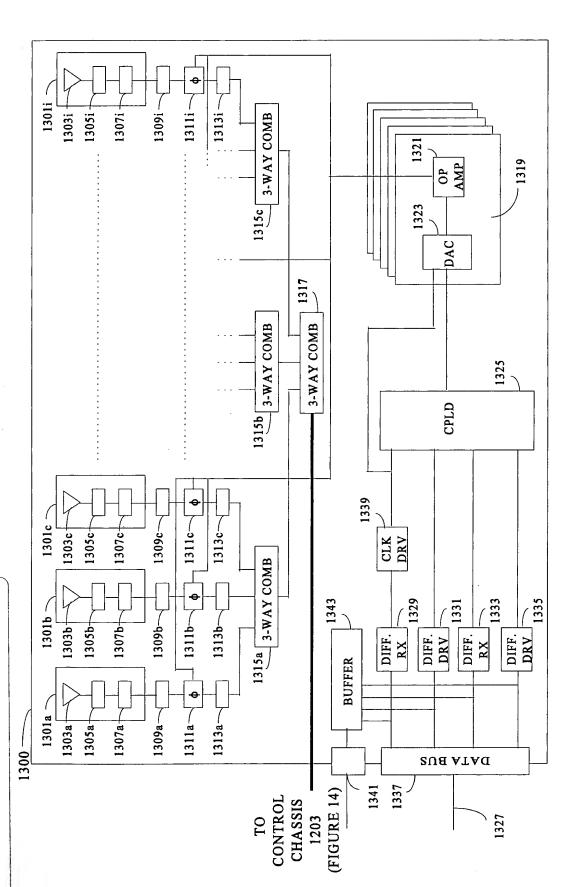


FIGURE 13

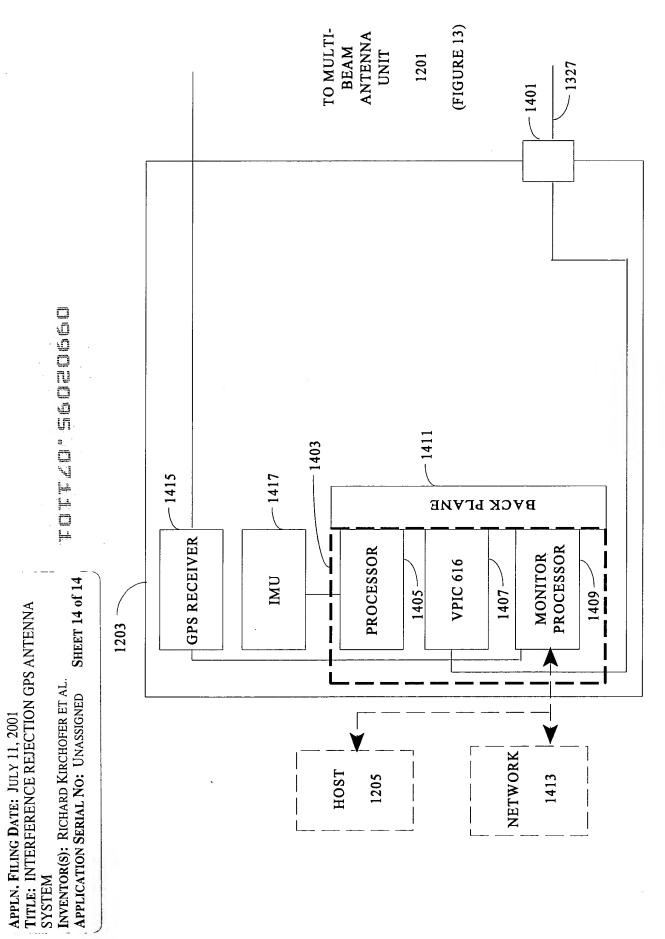


FIGURE 14